

Applicant: Hongwei KONG; Attorney docket 67108-089
EXPRESS MAIL NO. EV220476491US

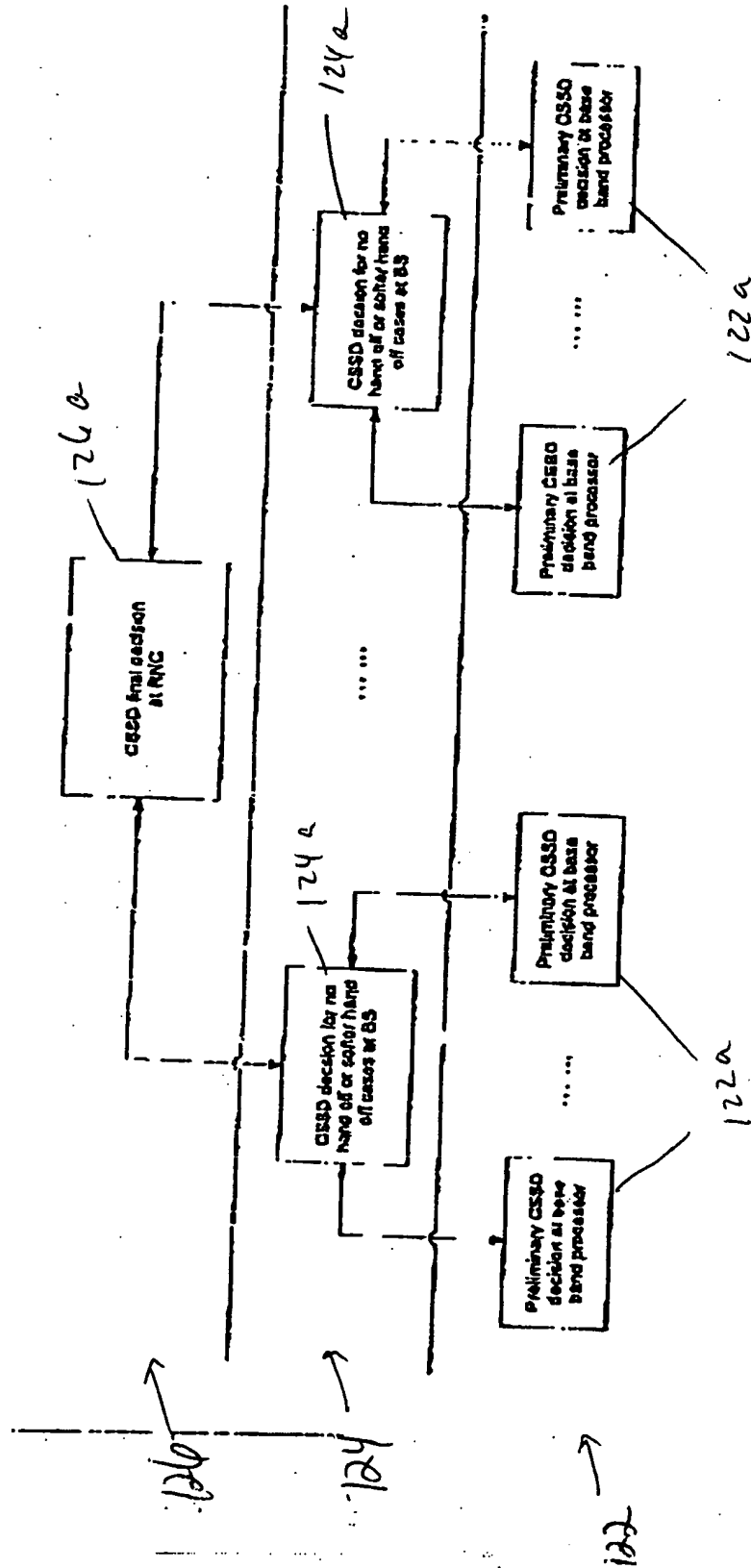
The diagram illustrates a system architecture. A central processing unit (BS, ASIC, Host) is connected to a PNC unit (116) and a network (101). The central unit is shown with a BS block, an ASIC block, and a Host block. The PNC unit is connected to the Host block. The network (101) is connected to the BS block. Various components are labeled with reference numerals: 102, 104, 105, 106, 114, and 115. A reference numeral 100 is also present.

Fig 1

200

2/5

Fig 2



3/5

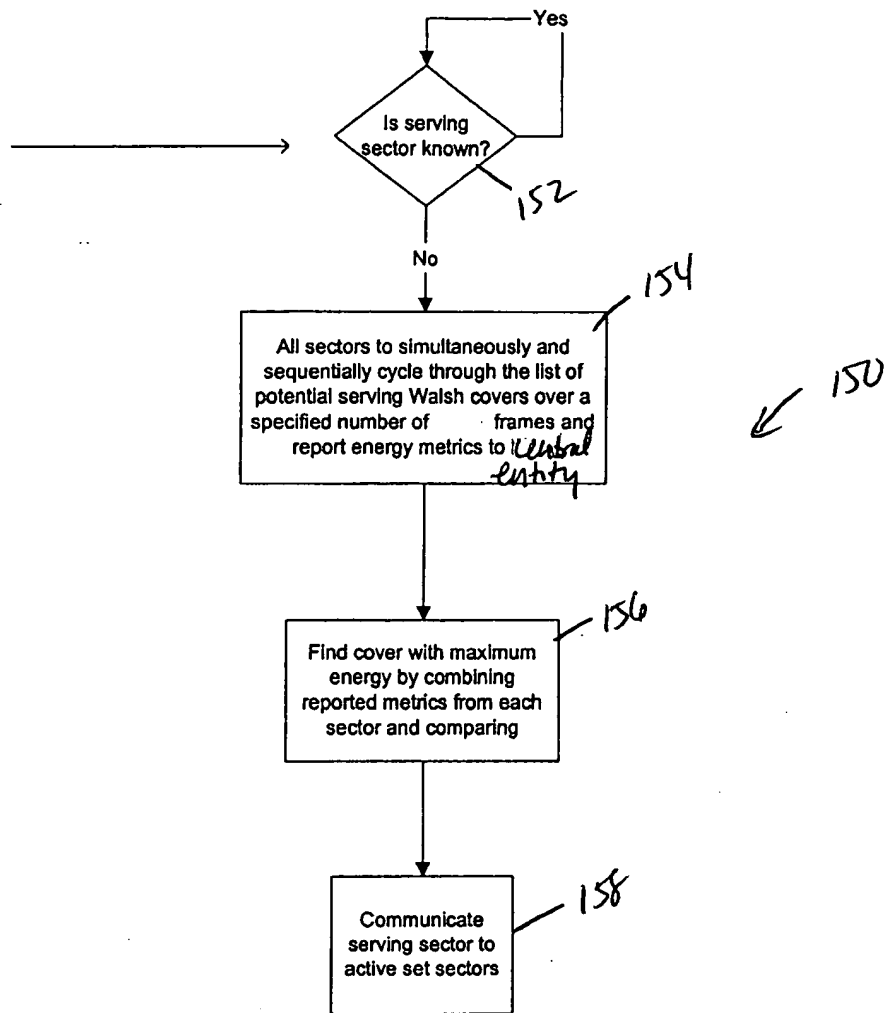


FIG 3

4/5

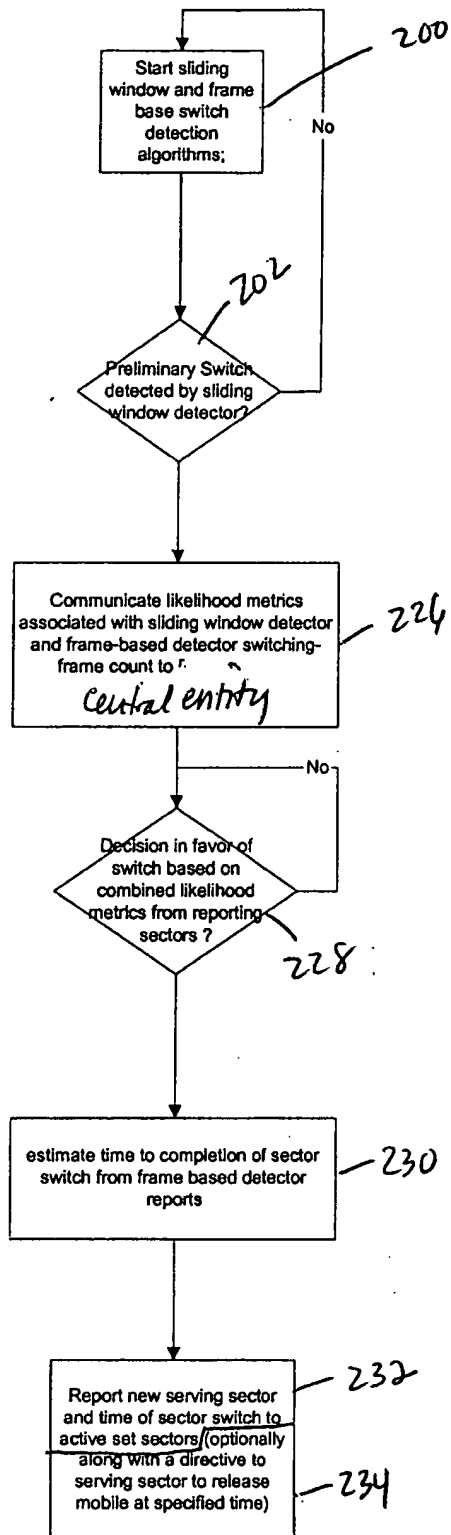


FIG 4

5/5

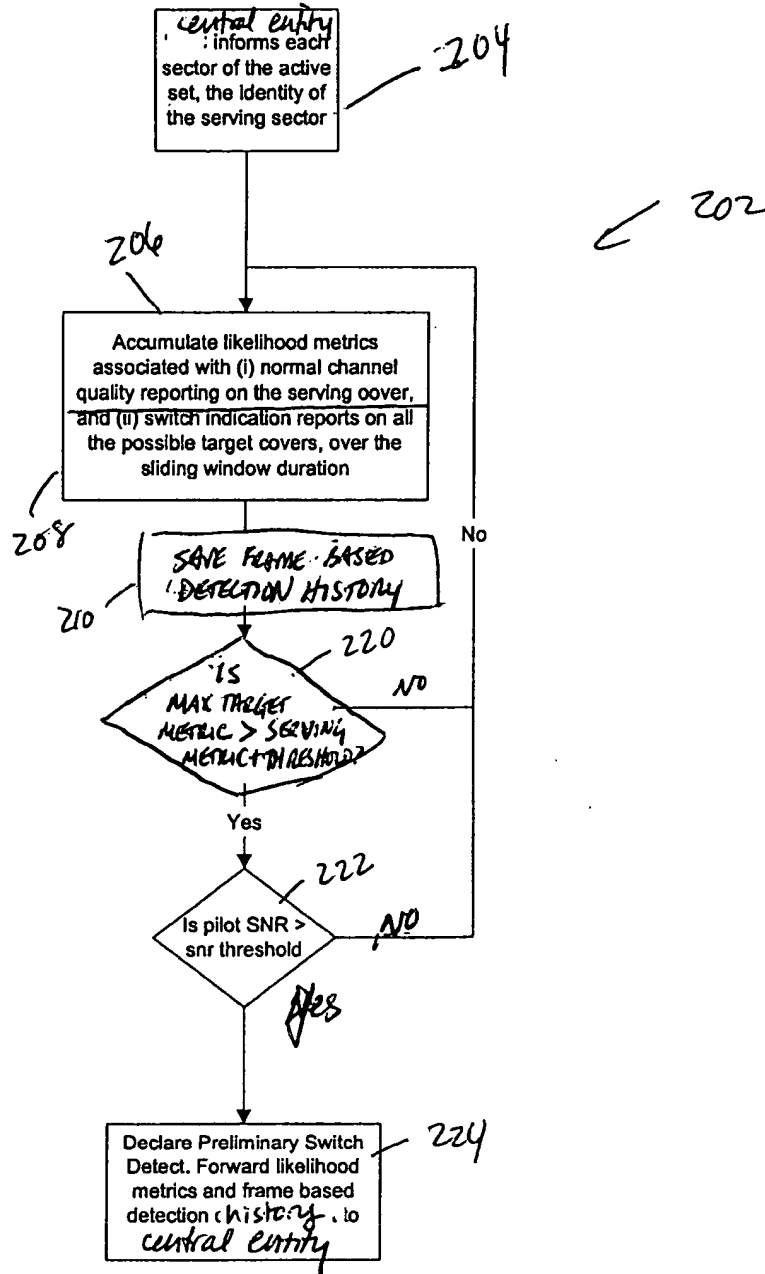


FIG 5